IN THE UNITED STATES PARENT AND TRADEMARK OFFICE

FEB 0 2 2005

In re Application of:

Russell Stuber

Serial No.:

10/037,722

Filed:

October 19, 2001

For:

Incremental Read Burst Size

Registry

Group Art Unit:

2182

**Examiner:** 

Kim, H.

**Atty Docket:** 

/ 01-265

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

February 2, 2005

Signature

## SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

## Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Data

MIOS

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331